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Your Search: `"save area layout"` [Search] **Advanced Web Search**
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TOP 6 WEB RESULTS out of about 6

1. [eros-cvs] cvs commit: eros/src/base/sys/arch/i486 LAYOUT
   ... @@ -23,11 +23,28 @@ * Register layout for x86. This file describes both the
   domain * layout and the **save area layout** for this architecture. ...
   www.eros-os.org/pipermail/eros-cvs/2001-February/000319.html cached

2. Calling Standard for Alpha Systems
   ... Figures. 3-1 Fixed Size Stack Frame Format 3-2 Variable Size Stack Frame
   Format 3-3 Register Save Area Layout 3-4 Register Save Area Example 4-1 In-
   Memory Homed ...
   www.lagu.net/docs/dec/AA-PY8AB-TET1_html/TOC.html cached

3. Jonathan Larmour - eCos anonymous CVS repository updated
   ... Reorganized register save area layout to match changes in + hal_arch.h. +
   Added macros sva, lva, mvatc0, mvafc0 to transfer 64 bit items to + and from
   memory ...
   sources.redhat.com/ml/ecos-cvs/2001/msg00006.html cached

4. Flow Control
   ... Figure 3-3 shows the layout of the register save area. Figure 3-3: Register Save
   Area Layout. RSA.SAVED_RETURN is the contents of the return address register.
   ...
   www.tru64unix.compaq.com/docs/base_doc/DOCUMENTATION/V51_HTML/ARH9MBTE/FLWCNTRL.HTI
   cached | more results from this site

5. PubTeX output 2000.03.10:1817 (PDF)
   ... ... 3-6 3-3 Register Save Area Layout ... ...
   h30097.www3.hp.com/docs/base_doc/DOCUMENTATION/V50A_PDF/ARH9MATE.PDF
   view as html | more results from this site

6. Hints and Tips for VSE/ESA (PDF)
   Page 1. IBM Hints and Tips for VSE/ESA Page 2. Page 3. IBM Hints and Tips for
   VSE/ESA Page 4. Note I The information contained in this ...
   www.ibm.com/servers/eserver/zseries/os/vse/pdf3/hint6mm2.pdf view as html

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(i) Did you mean: ESA/390 save area?

TOP 10 WEB RESULTS out of about 10

1. **Sketch of S/360 Subroutines — Mark Smotherman**
   ... Instruction set extensions along the way: S/370, S/370-XA, ESA/370, and
   ESA/390). ... pseudo-op to tell assembler to use r12 st r13,savearea+4 I store
   address of ...
   www.cs.clemson.edu/~mark/subroutines/s360.html cached

2. **Dave's S/390* Assembler FAQ page**
   ... Most of the ESA/390 instruction set and many of the IBM OS/390 and DFSMSdfp
   services and ... such as what to do if your caller provides a 72-byte savearea, and
   you ...
   www.planetmvs.com/hlasm/s390faq.html cached

3. **http://www.cs.niu.edu/csci/567/ho/pcrtn.ps** (POSTSCRIPT)
   ... contain length I Macros/SVCs require R13 to point to a savearea. ... 1991
   "Enterprise Systems Architecture/390"; Jeff Frye; IBM Corporation;"ESA/390
   Principles of ...
   www.cs.niu.edu/csci/567/ho/pcrtn.ps view as html | more results from this site

4. **Zee Guide to z/Architecture Zee Guide to z/Architecture** (PDF)
   ... in S/390 mode and a SIGP is issued to set the architecture mode to z/Architecture
   Bits 56-63 of parameter register specify mode: X'00' specifies ESA/390 mode X ...
   docs.share.org/proceedings/sh100/s2818.pdf view as html

5. **http://neuro2.med.uni-magdeburg.de/~markgraf/mvs/hercules-2.12n/vector.c**
   ... S/370 & ESA/370) */ /* SA22-7207-00 Vector Operations (ESA/390) */ /* 28/05 ...
   1) &= 0x0000FFFF; regs->GR_L(gr1 + 1) |= n1 << 16; /* Update savearea address
   */ regs ...
   neuro2.med.uni-magdeburg.de/~markgraf/mvs/hercules-2.12n/vector.c cached

6. **Getting Out of the Dumps with the VM Dump Tool** (PDF)
   ... pfxvcnfg +020C PFXFTO 7D7FF000 System frame table origin Note for ESA/390
   (not ESAME ... defined in CPEXSCHC (00) Bits defined in CPEXCALC (80) 80
   Savearea in use ...
   www.share.org/proceedings/sh98/data/S9369.PDF view as html

7. **TCP/IP for VSE/ESA IBM Program Setup and Supplementary ...** (PDF)
   ... The following terms are trademarks of the IBM Corporation in the United States or
   other countries or both: ACF/VTAM AIX AS/400 CICS CICS/VSE ESA/390 ESCON
   ES ...
   www.ibm.com/servers/eserver/zseries/os/vse/pdf2/vse261/iestce21.pdf view as html

8. **Programmer's Guide** (PDF)

Page 1. High Level Assembler for MVS & VM & VSE IBM Programmer's Guide
Release 3 SC26-4941-02 Page 2. Page 3. High Level Assembler ...
www.math.uni.lodz.pl/~miodekk/OS/390/asmp1002.pdf view as html

9. **CICS Transaction Server for z/OS BigCommareas; How to bypass
   the ...** (PDF)
   ... MVS DFSMSdfp DFSMSdss DFSMShsm DFSORT DXT eNetwork Enterprise
   Systems Architecture/370 Enterprise Systems Architecture/390 ES/3090 ESA/370
   ESA/390 ES/9000 ...
   www-3.ibm.com/software/ts/cics/library/whitepapers/BigCommarea.pdf view as html

10. **VSE Jun title copy** (PDF)
    ... B232RDR CSECT USING *,R15 ESTABLISH ADDRESSABILITY STM
    R14,R12,12(R13) SAVE CALLER'S REGISTERS LA R10,SAVEAREA ADDRESS
    OWN SAVE AREA ST R10,8(,R13) SAVE ...
    www.bimoyle.com/biminfo/pdfmags/june_99.pdf view as html

(i) Did you mean to search for: ESA/390 save area?

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TOP 20 WEB RESULTS out of about 185

1. **Linkage** 🗎
   ... Word 16. Contents of register 10. Word 17. Contents of register 11. Word 18. Contents of register 12. Save areas are chained in a double-linked list. ...
   hansen-family.com/mvs/Linkage.htm cached

2. **Bixoft Course 2 - High Level Assembler: Interfaces** 🗎
   ... Program linkages and save areas; Obtaining PARM data from the EXEC statement; QSAM macros and processing; SYNAD routines; Subroutines and the Linkage Editor; The ...
   www.bixoft.nl/english/course2.htm cached | more results from this site

3. **User's Guide - Assembler-Level Subroutine Linkage Conventions** 🗎
   ... The linkage convention allows for argument passing and return values to be in ... storage that is used to hold local storage, register save areas, parameter lists ...
   hpcf.nersc.gov/vendor_docs/ibm/xlf/html/UG79.HTM cached

4. **COBOL for MVS Dumps and Debugging** 🗎
   ... Module linkage CALL: passing parameters; ENTRY/GOBACK; CANCEL; flow of execution; save areas; using save areas; locating items in a called module. ...
   www.rsm.co.uk/courses/appd/cd.htm cached | more results from this site

5. **Subroutine Linkage** (PDF) 🗎
   ... Il look at one example here: the MIPS R2000 subroutine linkage conventions, but ... Save areas" When we talk about saving and restoring registers, what are we ...
   www.cs.ualberta.ca/~macg/C229/Notes/Lec12.pdf view as html | more results from this site

6. **Excalibur Training Services** 🗎
   ... Program Linkage, Conventions; The calling program; called program; Passing parameters; Save areas. Input/Output, Macros; Open; Close; Get; Put;DCB. ...
   www.btinternet.com/~excalibur.training/assembler_programming.htm cached | more results from this site

7. **Tips & Newsletters-Search390.com, a TechTarget Web site** 🗎
   ... Let's leave out STACK instructions and XPLINK linkage for now. We're just concerned with ages old MVS save area chaining. That's where save areas contain LSA ...
   search390.techtarget.com/tip/1,289483,sid10_gci862782,00.html cached

8. **Xephon Archives** 🗎
   ... _____ * | ACB | * |_____| **** LINKAGE SECTION. ... Search through * Save Areas till last one ...
   www.xephon.com/arcinframe.php/a012a02 cached | more results from this site

9. **Don't Let the DUMPS Get You Down** 🗎
   ... ABEND - REGS - Contents of general registers at entry to ABEND - SA - Requests save area linkage information and backward trace of save areas - SAH -

Requests ...
support.ca.com/techbases/datacom_ideal/25181.html cached

10. **C411 Outline** 🗎
    ... Program linkages Control Sections Save Areas Addressability Return Codes Typical Linkages SAVE ... Subroutines and the Linkage Editor CSECTs and the Linkage Editor ...
    www.trainersfriend.com/C411O.htm cached | more results from this site

11. **http://www.fsa.usda.gov/debes/MDBC9007.txt** 🗎
    ... 01610054 * *01620054 * COUNTERS AND SAVE AREAS *01630054 * *01640054 ... 01720054 * *01730054 * LINKAGE SECTION *01740054 * *01750054 ...
    www.fsa.usda.gov/debes/MDBC9007.txt cached

12. **MVS 3** 🗎
    ... SA or SAH, Save area linkage information and backward trace of save areas. (This options is selected by ALLPDATA). SAH requests only ...
    www.tommysprinkle.com/mvs/leaabd00.htm cached

13. **Dave's S/390 Assembler FAQ - Top Ten** 🗎
    ... Scott sent me this one: Program uses R2 as a base or linkage register; You ... the "Save Area Trace" portion of the SYSUDUMP output (if you chain your Save Areas).
    ...
    www.planetmvs.com/hlasm/topten.html cached

14. **IBM IT Education Services - United Kingdom - ASM02GB Advanced ...** 🗎
    ... input o How to code I/O Operations in Assembler o How to write re-entrant code o How to establish save areas under the standard linkage conventions o How to ...
    www.ibm.com/services/learning/uk/ta-iris.nsf/(ExtCourseNr)/ASM02GB cached | more results from this site

15. **Subprograms** (MICROSOFT POWERPOINT) 🗎
    ... Must retain status in static location. Subprogram Linkage. Finding Entry Point: ... How does routine return to Caller? Static Save Areas. STM 14,12,12(13). ...
    www.personal.psu.edu/faculty/w/l/wlg114/CSE428/Subprograms.ppt view as html

16. **Mainframe Week Issue 26, 3 July 2002. Analysing DSNC abends** 🗎
    ... which SQL statement caused the error, we have to go through the save areas until we ... by COBOL to address the fields in the working storage and linkage section. ...
    www.mainframeweek.com/journals/articles/0026/Analysing+DSNC+abends cached

17. **CICS V2 TO V4 FOR APPLICATIONS PERSONNEL** (PDF) 🗎
    ... Fax: 01273 731345 c cc o oo u uu r rr s ss e ee f ff a aa c cc t tt s ss h hh e ee e ee t tt Calls and linkage Conventions; save areas; parameters; addressing. ...
    www.circle-group.com/training/as_2003.pdf view as html

18. **8.16 Restrictions** 🗎
    ... of COBOL working storage, USER GETMAIN areas, CICS GETMAIN areas, register save areas, etc. ... Examples: Use COBOL LINKAGE SECTION instead of WORKING STORAGE. ...
    www.state.sd.us/standards/Section%208-16.htm cached

19. **Sketch of S/360 Subroutines — Mark Smotherman** 🗎
    ... byte 24: contents of r1 ! ... ! byte 68: contents of r12. Further discussion. You must

dynamically allocate save areas to support recursion. ...
www.cs.clemson.edu/~mark/subroutines/s360.html cached

20. **Using Assembler Language at the NIH Computer Center - September ...** 🗎
    ... A detailed description of OS/VS Binder conventions, save areas, and parameter lists may be ... registers 0,1,13,14, and 15 are known as the linkage registers and ...
    datacenter.cit.nih.gov/assem/assem-07.htm cached

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rugarea.com

TOP 20 WEB RESULTS out of about 1,650

1. Forest thinning expected to save areas
Forest thinning expected to save areas. Project director confident of efforts north of blaze. Mark Shaffer The Arizona Republic Jul. 16, 2003 12:00 AM. ...
www.azcentral.com/specials/special38/articles/0716fire-thinning16.html cached | more results from this site

2. Tree Save Areas
... Tree Save Areas. From: Citizens Tree Coalition Date: 05 Mar 2002 Time: 14:03:59. Comments. The Woodlands Preservation Standards were ...
trees.ci.sat.tx.us/_disc1/00000035.htm cached | more results from this site

3. Tree Save Areas in Kentlands
Tree Save Areas in Kentlands. ...
www.greenspun.com/bboard/q-and-a-fetch-msg.tcl?msg_id=003zvQ cached

4. DUMPSCAN FINDUSER Help Information
... FILE-' '-AT-address—— Purpose Use the FINDUSER subcommand (macro) to determine the CP module flow for a hung virtual machine (if the save areas can be ...
www.ucc.uconn.edu/cgi-bin/cmshelp?DUMPSCAN%20FINDUSER cached | more results from this site

5. Claritas : t1
... This feature allows you to: define your areas only once; create an unlimited number of areas; permanently save areas until you decide to delete them. ...
cluster1.claritas.com/MyBestMarkets2/Default.wjsp?ID=41&SubID=40 cached | more results from this site

6. NeverwinterConnections.com
... I don't know how to automate the counting of the number of save areas. ... This script needs to be updated as new save-areas are added. (See Script 2 in Code Box). ...
www.neverwinterconnections.com/scripts/index.cfm/fuseaction/viewscript/id/20.htm cached

7. Welcome To Document Imaging Solutions, Inc.
... The Flotiva Application Builder has a entire command set dedicated to the Current Database Object. What is a Flotiva Save Areas? ...
www.disolutions.com/knowbase_1.html cached

8. Excalibur Training Services
... Program Linkage, Conventions; The calling program; called program; Passing

parameters; Save areas. ... Debugging, Assembler listing;Link edit listing; Save areas. ...
www.btinternet.com/~excalibur.training/assembler_programming.htm cached

9. § 3-4 Tree Protection (MICROSOFT WORD)
... 3-4-3.1 Applicability. §3-4-3.2 Tree Save Areas. §3-4-3.3 Canopy Cover Requirements. §3-4-3.4 Replacement Trees. ... 3-4-3.2 Tree Save Areas. ...
www.dca.state.ga.us/planning/ModelCode/3-4TreeProtection.doc view as html | more results from this site

10. Bixoft Course 2 - High Level Assembler: Interfaces
... SC26-4916 - Program Management. Major Topics Include: Program linkages and save areas; Obtaining PARM data from the EXEC statement; ...
www.bixoft.nl/english/course2.htm cached | more results from this site

11. Ask the Experts - Search390.com, presented by TechTarget
... Well, you can muck around with save areas, but this is not documented and could be unreliable if we change things around. Currently ...
search390.techtarget.com/ateQuestionNResponse/0,289625,sid10_cid506960_tax285603,00.html cached

12. Linkage
... Word 16. Contents of register 10. Word 17. Contents of register 11. Word 18. Contents of register 12. Save areas are chained in a double-linked list. ...
hansen-family.com/mvs/Linkage.htm cached

13. Rainforest Site
donate land for free.
www.therainforestsite.com/
↳ More sites about: Tropical Rainforest Organizations

14. Construction: Process Mapping
... Mark tree save areas and rope off other areas to remain undisturbed and hydrologically intact during construction. 5.2, 1, Recommended, See above. Site Credit 6.1 ...
www.afceo.brooks.af.mil/dc/dcd/arch/rfp/s04construction/process01.html cached | more results from this site

15. COBOL for MVS Dumps and Debugging
... Module linkage CALL: passing parameters; ENTRY/GOBACK; CANCEL; flow of execution; save areas; using save areas; locating items in a called module. ...
www.rsm.co.uk/courses/appd/cd.htm cached | more results from this site

16. Castle Wilderness - Protected Areas
... Fortunately, opportunities still exist for Albertans to save areas of international, national and provincial calibre such as the Castle Wilderness. .. ..
www.castlewilderness.ca/protected.html cached

17. Using the indep Option for Interlanguage Communication : Sample ...
... discuss an example of calling C indep code from PL/I. The C code in turn calls PL/I. Sample L$URTOP Routine manipulates bits in PL/I save areas to prevent ...
support.sas.com/documentation/onlinedoc/sasc/doc/clug/zndepsam.htm cached | more results from this site

18. INTRODUCTION (MICROSOFT WORD)
... Our redesign in order to make more distinguishable the Search and Save areas

still does not work very well: users mention that at first it looks as if they ...
www.sims.berkeley.edu/academics/courses/is213/s03/projects/chronkite/8.doc view as html

19. Best Practices Management
... Saving a tree involves more than leaving it standing. Tree "save areas" need to be established, and root damage must be prevented as much as possible. ...
www.treescolumbus.org/bestManagementPractices.htm cached

20. Don't Let the DUMPS Get You Down
... the error occurred - Contents of the load module - Control blocks related to the failing task - Recovery termination control blocks - Save Areas - Registers at ...
support.ca.com/techbases/datacom_ideal/25181.html cached

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128-bit encryption

Your Search: communication savearea   [Search]   Advanced Web Search / Preferences

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TOP 20 WEB RESULTS out of about 117

1. **Communication with Assembler Programs : Calling aC Program from ...**
   ... Calling aC main Function from Assembler via $MAINO MAINASM CSECT STM
   14,12,12(13) standard OS entry linkage BALR 9,0 USING *,9 LR 14,13 LA
   13,SAVEAREA ST 14,4 ...
   support.sas.com/documentation/onlinedoc/sasc/doc/clug/zasscp.htm cached | more
   results from this site

2. **Lecture 11: Cache and Memory** (MICROSOFT POWERPOINT)
   ... Preview Potential Pernicious Process Problems: inter-process communication.
   scheduling. deadlock. ... buffer. */. }. sioint_stub: sw [savearea], R0. sw
   [savearea+4], R1. ...
   www.cc.gatech.edu/classes/cs3431_99_winter/lectures/990112-proc.ppt view as
   html | more results from this site

3. **Xephon Archives**
   ... LR R11,R15 LOAD BASE ADDRESS. L R3,0(R1) GET COMMUNICATION AREA
   ADDRESS. GETMAIN R,LV=AREALEN GETMAIN A REGISTER SAVEAREA.
   USING WORKAREA,R13 SET AREA BASE. ...
   www.xephon.com/arcinframe.php/n003a06 cached | more results from this site

4. **http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt** (MICROSOFT
   POWERPOINT)
   ... void ExitProcess(int exitCode);. interprocess communication (IPC) related. ...
   Note SaveArea, ProcessDescriptor, & interrupt vector pointers. ...
   www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt view as html

5. **Mainframe Week - Code db2**
   ... RUN EXTERNAL ROUTINES * * CLOSE - CLOSE A DATASET * * DCB - DATA
   COMMUNICATION BLOCK * * GET ... ENTRY POINT USING I258,R12 USE R4 AS
   BASE XC SAVEAREA(72),SAVEAREA ...
   www.mainframeweek.com/code/showcode.php/0015/mw15db23.txt cached | more
   results from this site

6. **Using the 3494 Tape Library under VSE/ESA**
   ... Figure 3. Communication paths *---- VSE/ESA ... R10,TCATEG LA
   R13,SAVEAREA USING IJJLBSER ...
   www.ibm.com/servers/eserver/zseries/os/vse/support/af/vse5sup.htm cached | more
   results from this site

7. **Parallel Systems** (MICROSOFT POWERPOINT)
   ... Inter-thread communication and synchronization is very fast. ... int timeLeft; int
   nextThread, prevThread; int pid; ThreadState state; SaveArea sa; }; struct ...
   www.cs.unm.edu/~crowley/osbook/slides/chap06.ppt view as html

8. **http://www.geocities.com/SiliconValley/Vista/9167/Sources/cmd101.txt**
   ... R13,SA3+4 SAVE A(OLD SAVEAREA) LR R12,R13 LA R13,SA3 ST R13,8(R12)
   SAVE A(NEW SAVEAREA) * ICM R05,15,8(R01) USING MCSCOMM,R05 Set
   Communication DSECT * *+ TPUT ...
   www.geocities.com/SiliconValley/Vista/9167/Sources/cmd101.txt cached | more
   results from this site

9. **http://www.freeusp.org/Arco/Data/sparc/movwd**
   ... 00003800 * ==== 00003900 * 00004000 R0 EQU 0 SUBROUTINE
   COMMUNICATION 00004100 R1 ... R12,12(R13) SAVE REGISTERS 00006600 LA
   R12,SAVEAREA POINTER TO ...
   www.freeusp.org/Arco/Data/sparc/movwd cached | more results from this site

10. **Tips & Newsletters-Search390.com, a TechTarget Web site**
    ... Change routing information (IP address?) for priority communication. ... Store
    ANCHOR into CVTUSER or into TCBUSER or into first word of JOB STEP TCB's
    savearea. ...
    search390.techtarget.com/tip/1,289483,sid10_gci915653,00.html cached

11. **Bixoft - Macro library - Bixoft eXtended Assembly language**
    ... OUCB, MAPOUCB, ResOurces manager User Control Block. PCCA, MAPPCCA,
    Physical Configuration Communication Area. ... SAVEAREA, MAPSAVE, Standard
    SAVE AREA. ...
    www.bixoft.nl/english/maclib.htm cached

12. **http://www.itc.virginia.edu/~drs8h/articles/cpuupdt.txt**
    ... ESTABLISH USING CPUSCAN,R12 ADDRESSABILITY LR R12,R15 LA
    R11,SAVEAREA ST R11 ... ENTRY FOR ASCBS * * * OPERCOMM LA
    R9,COMFLD SET UP OPER COMMUNICATION AREA EXTRACT ...
    www.itc.virginia.edu/~drs8h/articles/cpuupdt.txt cached

13. **VSE Dec title** (PDF)
    ... SAVEAREA DS 9D EJECT ***** CROSS-PARTITION COMMUNICATION
    CONTROL BLOCK (MODIFIED ...
    www.bimoyle.com/biminfo/pdfmags/dec_97.pdf view as html | more results from this
    site

14. **Modern CICS Application Development: The Big Picture** (PDF)
    ... JSP's Java Servlet EJB MQSeries Web Servers Appl Servers Databases
    Transactional Systems Communication Enterprise Applications Page 4. ...
    www-3.ibm.com/software/htp/tsvcon2001/cicscd/KeyView.pdf view as html | more
    results from this site

15. **http://www.cbttape.org/uclamail/ipcprog.txt**
    ... Communication link speed is also assumed to be fast enough to avoid requiring
    special ... rest of the registers are undefined, R13 does NOT point to a savearea. ...
    www.cbttape.org/uclamail/ipcprog.txt cached

16. **Introduction To Programming In An IBM Environment** (PDF)
    ... AND TRANSACTIONS Multiregion Operation Intersystem Communication
    Transaction ID ... the Base and Displacement Addressing mechanism, and Register
    Savearea chaining. ...
    www.dki.com/pdf/datatrainpdfs/intro.pdf view as html

17. **ROSAM - Programmerare Handbok**

... Om man inte lyckas lokalisera applikationsprogrammets savearea får man
försöka ... finns ett kontrollblock kallat SCA, System Communication Area,
innehållande ...
www.rosam.se/arosdoc/rspgmr/34.htm cached

18. **http://ftp.sas.com/samples/SASC6.0/clug/chap11**
    ... Fourth Edition, Release 6.00 */ /* Communication with Assembler ... 14,13 LA
    13,SAVEAREA ST 14 ...
    ftp.sas.com/samples/SASC6.0/clug/chap11 cached

19. **Error handling for SESAM V2.2**
    ... CTAB. Communication table. FTAB. ... Distance X'0C' à Registers 14 to12.
    Distance X'48' à Register 13. The address of the SAVEAREA is transferred in
    register 13. ...
    www.its.siemens.de/lobs/its/techinf/oltp/sesam/dokumentation/fehler_en.htm cached

20. **VSIOF001 Assembly**
    ... 05770000 000C10 1316 SAVEAREA DS 18F 05780000 1317 ... 06100000 1365 *
    THE DUMMY SECTION BELOW MAPS THE COMMAND COMMUNICATION
    PARAMETER * 06110000 1366 * BLOCK ...
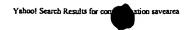   www.jaymoseley.com/hercules/compiling/VSAMIO.htm cached

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(i) Did you mean: context save area?

TOP 20 WEB RESULTS out of about 117

1. http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt (MICROSOFT POWERPOINT)
   ... - reschedule a process. Save the context of current process, if there was one (page133). SaveArea *savearea = &(pd[current_process].sa);. asm { ...
   www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt view as html | more results from this site

2. http://www.sinenomine.net/downloads/openafs-1.0.2-s390.diffs
   ... else savecontext(Dispatcher, &(temp -> context), &(LWPANCHOR ... 1 *****
   85,97 **** #if defined(DEBUG) { int l, *ptr = (int*)savearea->setjmp_buffer ...
   www.sinenomine.net/downloads/openafs-1.0.2-s390.diffs cached

3. http://www.mit.edu/afs/athena/project/net_dev/events/lwp/lwp.c
   ... int (*ep)(), struct lwp_context *savearea, char *sp); Stub for Assembler routine that will save the current SP value in the passed context savearea and call ...
   www.mit.edu/afs/athena/project/net_dev/events/lwp/lwp.c cached

4. Python: module rox.saving
   ... A SaveArea contains the widgets used in a save box ... drag_begin(self, drag_box, context) drag_data_get(self, widget, context, selection_data, info, time) drag_end ...
   rox.sourceforge.net/rox_lib_docs/rox.saving.html cached

5. http://openbsd.secsup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c
   ... Stub for Assembler routine that will * save the current SP value in the passed * context savearea and call the function * whose entry point is in ep. ...
   openbsd.secsup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c cached

6. Re: Porting Arla to Linux/390
   ... void savecontext(void (*)(void), struct savearea *context, void *new_sp); void returnto(struct savearea *context); savecontext saves all necessary processor ...
   www.stacken.kth.se/lists/arla-drinkers/2000-07/msg00034.html cached | more results from this site

7. eor.dublincore.org - TView
   ... td></tr> + <% +table> + <% } 1.1 eor/src/context/jsp/login ... String eorUser = getCookie(request, "eorUser"); + String saveArea = null; + String ...
   eor.dublincore.org/eor-cvs/msg00125.html cached

8. Lecture 1: Course Introduction and Overview (MICROSOFT POWERPOINT)
   ... 2. run handler. Mackenzie Spring'03. Context Switch in Handler. void xxx_handler (void). { /*. * need to switch. * processes? * change 'savearea'. */. }. xxx_stub: ...
   www.cc.gatech.edu/classes/AY2003/cs2200_spring/Slides/Lec10-proc.ppt view as html | more results from this site

18. port-mac68k: Re: Kernel
    ... f_regs, sizeof(tf->f_regs)-2*NBPW); /* * Restore the original FP context */ @@ -372,15 ... the user SP movl a0,sp@(FR_SP) | in the savearea - movl #SYS_sigreturn,sp ...
    mail-index.netbsd.org/port-mac68k/1998/09/24/0021.html cached

19. Assembler Coding in the z/OS Environment: Part I (PDF)
    ... sections, I will discuss how to use these instructions in the context of assembler ... After the BSM, you will replace R14 (31-bit version) in the savearea. ...
    www.naspa.com/PDF/2002/0202%20PDF/T0202004.pdf more results from this site

20. http://www.iuassn.com/UCL/viewdb01.txt
    ... LINKAGE DSECT DS H PARMCVN DS CL3 VS24BIT DSECT SAVEAREA DS 18F ... SLACK DS CL42 BUFACVN DS CL3 BUFFHEAD ORG BUFFER /* Header record context map —— DS ...
    www.iuassn.com/UCL/viewdb01.txt cached

(i) Did you mean to search for: context save area?

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9. NAME:
   ... restore the state of another (possibly the same) process (the half of a context switch that occurs at the end of the Dispatcher()). (AM) saveArea – Part of ...
   websrv.cs.fsu.edu/~jtbauer/cop4610/midterm_soln.html cached

10. [OpenAFS-devel] state of the (development) world
    ... Today on ia64 + Linux, lwp uses "context() from > /usr ... update stack pointer register for this platform" 48 #endif 49 50 PRE_Block = 1; 51 52 savearea->state = 0 ...
    https://lists.openafs.org/pipermail/openafs-devel/2003-May/004317.html cached | more results from this site

11. [eros-cvs] cvs commit: eros/src/base/sys/scripts GenFixRegs.awk
    ... hxx UserContext.cxx UserContextIPC.cxx src/base/sys/eros/i486 SaveArea.h src/base ... CS & ~0x3u) == 0x18)) { /* returning to non-kernel context */ MsgLog::printf ...
    www.eros-os.org/pipermail/eros-cvs/2001-June/000557.html cached | more results from this site

12. MULTITASKING 8051 CAMELFORTH (PDF)
    ... The code presented here clearly shows the extra context-switching overhead of a preemptive ... mov a,r2 movx @dptr,a inc dptr mov a,dph ; UAREA=SaveArea+100h, so ...
    www.zetetics.com/bj/papers/8051task.pdf view as html

13. BBC BASIC and the in-line assembler
    ... Remember also that the operators in a dyadic context are symmetric, so a ... For instance, suppose we define: DEF FNsave_regs(savearea) | OPT pass id (savearea),hl ...
    www.zxplus3e.plus.com/z88forever/dn327/basicasm.htm cached

14. ispell_checker.h Source File
    ... were formerly global functions 00044 // passed a context structure pointer ... P ((ichar_t * word, ichar_t * pattern, 00067 ichar_t savearea[MAX_CAPS] [INPUTWORDLEN ...
    www.abisource.com/doxygen/ispell__checker_8h-source.html cached

15. http://cvs.pld.org.pl/ispell/Attic/correct.c?rev=1.64
    ... Also display tab characters * correctly when showing context. ... int save_cap P ((ichar_t * word, ichar_t * pattern, ichar_t savearea[MAX_CAPS] [INPUTWORDLEN ...
    cvs.pld.org.pl/ispell/Attic/correct.c?rev=1.64 cached | more results from this site

16. Re: PowerPC FP handling weakness.
    ... Some are really out of context, so it is best to ignore them ... timebaseData record nextTickH ds.l 1 nextTickL ds.l 1 tickCount ds.l 1 endr saveArea record SavedSP ...
    www.rtems.com/rtems/maillistArchives/rtems-users/2000/october/msg00055.html cached

17. http://www.klid.dk/pub/gnu/gnu-utils/gnu/ispell-3.120/correct.c
    ... firstdent, struct flagent * pfxent, struct flagent * sufent, ichar_t savearea [MAX_CAPS][INPUTWORDLEN ... blank line between the * columns and the context, plus a ...
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   resource for books, reports, journal articles, newspaper articles, news analysis,
   maps, conference papers, and more.
   popenvironment.org/ cached
   ↳ More sites about: Urban Studies > Web Directories

2. **Linkage Service FAQs**
   DataWindow Linkage Service FAQ. Table of Contents. ... In which event should I
   enable and configure the linkage service? Typically PFC ...
   www.pfcguide.com/faq/faq_link.asp cached | more results from this site

3. **Providing strategic linkage service to candidates running for ...**
   Providing strategic linkage service to candidates running for office to depict and
   highlight their positions guided by our motto To serve, To discover, To ...
   www.conservery.com/CandidateFlyer.htm cached

4. **Maintenance Guide for the Linkage Service 19 July 2000** (PDF)
   1 Maintenance Guide for the Linkage Service 19 July 2000 Once you have created
   the journal set for your institution and the system is 'live' you must now ...
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5. **ISI Web of Science Linkage Service Frequently Asked Questions - ...**
   (PDF)
   Page 1. 1 ISI Web of Science Linkage Service Frequently Asked Questions - 16
   May 2000 How do I subscribe to the Linkage Service? ...
   wos.mimas.ac.uk/docs/linkagefaq.pdf view as html | more results from this site

6. **Community Access/Linkage Service**
   Community Access/Linkage Service. ... The Community Access/Linkage service.
   The Community Access/Linkage service (aCe-Link) supports people ...
   www.dhcs.act.gov.au/disability/Services/Community.htm cached | more results from
   this site

7. **PowerBuilder Foundation Class Library User's Guide**
   ... Linkage service. Overview. The PFC linkage service helps you to create
   master/detail windows and other types of windows that require coordinated
   processing. ...
   sybooks.sybase.com/onlinebooks/group-
   pb/pbg0900e/pfcug/@Generic__BookTextView/5010 cached | more results from this
   site

8. **WEB SITE LINKAGE SERVICE**
   info.fujita-hu.ac.jp/~tkohama/Web-site.html cached | more results from this site

9. **PowerBuilder - PFC Linkage Service Example**
   ... PFC Linkage Service Example. Tip by: Jesus Gonzalez JGonzalez@CHA-

SYSTEMS.COM. This is a good example of how to implement the PFC Linkage
Service. ...
www.justpbinfo.com/pbtips/pfclinkage.asp cached

10. **Linkage Service**
    President Profile, リンケージサービス株式会社. ...
    www.lsi-gpc.co.jp/president.htm cached | more results from this site

11. **Clutch Linkage Service Kit**
    Mopar Performance Parts & Aftermarket parts for DaimlerChrysler Cars & Trucks.
    We are the Original Mopar Performance Outlet established ...
    shop.store.yahoo.com/chucker54/clutlinserki.html cached

12. **ThaiFactory Homepage Linkage : Service & Consultancy**
    ThaiFactory Homepage Linkage ——Select Category——. ...
    www.thaifactory.com/FactoryLinkage/ServiceEn.html cached

13. **How to use the Linkage Service**
    How to use the Linkage Service. Enable linkage for Master DataWindow. ...
    dw_detail.of_LinkTo(dw_master). Call the linkage service functions as needed. ...
    www.e-cool.com/pfc/DataWindow%20Services/tsld021.htm cached | more results
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14. **WEB SITE LINKAGE SERVICE**
    www.fujita-hu.ac.jp/~tkohama/Web-site.html cached

15. **ACT Community Care - Community Access/Linkage Service (aCe-
    Link ...**
    The Disability Program is no longer a part of ACT Community Care. For Disability
    services, please visit the ACT Department of Disability ...
    www.communitycare.act.gov.au/disability/aCe.html cached

16. **Services & Products > 소프트웨어 > WAS/EAI > Application ...**
    ... CORBA service (compliant with CORBA 2.3 and 2.4), (*2), (*2), Database linkage
    service (ObjectTransactionService), -, -, -, -,. CORBA-WWW server linkage function,
    -, -, -..
    kr.fujitsu.com/products/software/internet_base/application_server/look_function/
    cached

17. **Untitled Document**
    LIVING LINKAGE SERVICE 34A Maitland Street - Bingara. Who is Sunnyfield?
    Since 1951 ... serves. What is the Living Linkage Service? The ...
    www.bingara.nsw.gov.au/business_guide/sunnyfield.html cached

18. **Web of Science linkage Meeting 4** (MICROSOFT WORD)
    ... In compiling this document I consulted representatives of all 45 UK academic sites
    which subscribe to the Web of Science linkage service; the JIBS User Group ...
    www.jibs.ac.uk/resources/wos/linkage.doc view as html | more results from this site

19. **Web Technology Service Provider - Web Page Design**
    ... Chinese); One e-mail account set up and linkage service; Registration of 1000
    Sarah engines. 10-Page Web Page Design HK$4,388. Sign up now. ...
    www.webtsp.com.hk/English/Service/e_webpagedesign.htm cached

20. **CHEST / BIOSIS Agreement: Service Providers**
    ... 5,000 (full file, 1969 to present), no additional charge, 20 pooled simultaneous

users - may be reviewed if necessary, n/a, no additional charge for linkage
service ...
www.chest.ac.uk/datasets/biosis/service-providers.html cached

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   ... The remainder of these slides will look at how parameter lists, saveareas, and registers perform together as a system to make standard linkage work. ...
   Saveareas. ...
   www.math.uni.lodz.pl/~miodekk/OS/390/ppt/13extsub.ppt view as html

2. **http://wwwold.ifi.uni-klu.ac.at/Modula-3/m3pc.94/doc/m3edit/m3edit-impldoc.txt**
   ... Caret, cursor, pointer GRX maintains saveareas as it moves such things. In case of more cursors, care must be taken if saveareas are overlapped. ...
   wwwold.ifi.uni-klu.ac.at/Modula-3/m3pc.94/doc/m3edit/m3edit-impldoc.txt cached | more results from this site

3. **Zee Guide to z/Architecture Zee Guide to z/Architecture** (PDF)
   Page 1. 1 Zee Guide to z/Architecture Copyright © RP Shannon 2003 RP Shannon John Hancock Financial Services 200 Berkley Street ...
   docs.share.org/proceedings/sh100/s2818.pdf view as html

4. **Формат are-файлов для World of Merlin**
   ... плановыми врямями. Параметр -saveareas заставляет сохранить все *.are файлы. Если ...
   users.univ.kiev.ua/~tanidar/mudsable/addon/worn30.htm cached

5. **CPCS V1R12 APARS**
   ... (P18271), IBM, AQ70049 detail. DKNMREAD, PCTL abends subpass. . This APAR corrects a problem with DKNPLINK not creating its internal saveareas in the MVS standard. ...
   www.sei-mail.com/CPCS_R12/guru1297.htm cached | more results from this site

6. **http://wuarchive.wustl.edu/mirrors/NetBSD/NetBSD-current/xsrc/xfree/xc/programs/Xserver/mi/mibank.c**
   ... pScreen = pPixmap->drawable.pScreen; RegionRec rgnClipped; int i;
   SCREEN_INIT; SCREEN_SAVE; SCREEN_UNWRAP
   (BackingStoreFuncs.SaveAreas); if (IS_BANKED(pWin ...
   wuarchive.wustl.edu/mirrors/NetBSD/NetBSD-current/xsrc/xfree/xc/programs/Xserver/mi/mibank.c cached | more results from this site

7. **Horrible Things You Can Do With IDCAMS!**
   ... There is a technique to avoid both the HLL callbacks and the staging of data. Yes, it's the old "seesaw" trick with saveareas. This ...
   www.multi-platforms.com/Tips/HorribleIDCAMS1.htm cached

8. **Xephon Archives**
   ... Obtain Dynamic Working Storage and Chain Saveareas * *— GETSTOR DS 0H ...
   www.xephon.com/arcinframe.php/f006a05 cached | more results from this site

9. **WRP63**
   ... Two slots are used to 'chain' the saveareas, one points to the previous save area and one to the next, if and when the module calls yet another module. ...
   home.x.net.com/~msk/futuresource/hamasaki/wrp63.html cached

10. **Strategies for Porting the X v11 Sample Server** (PDF)
    Page 1. Strategies for Porting the X v11 Sample Server Susan Angebrandt Raymond Drewry Philip Karlton Todd Newman minor revisions ...
    www.msu.edu/~huntharo/xwin/docs/xwindows/strat.pdf view as html | more results from this site

11. **http://freebsd.ntu.edu.tw/doc/clement/Emacs**
    ... Emacs*XlwMenu.packPacket.labelString: I
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    Emacs*XlwMenu.saveAreas.labelString: ! ...
    freebsd.ntu.edu.tw/doc/clement/Emacs cached

12. **Introduction To Programming In An IBM Environment** (PDF)
    ... HANDLE CONDITION Entry ABEND Codes Module 6 IBM ADDRESSING AND USE OF REGISTERS A History of Addressing Base and Displacement Addressing SAVEAREAs Return Codes ...
    www.dki.com/pdf/datatrainpdfs/intro.pdf view as html

13. **Gammon Software Solutions forum**
    ... time to shut down the server. I make sure I force everyone to save, saveareas, ect and then do a shutdown. I then try useing the ...
    www.gammon.com.au/forum/bbshowpost.php?bbsubject_id=568 cached

14. **A1 Studietaak 10**
    ... (Het is overigens niet eenvoudig dit exact vast te leggen. Bijvoorbeeld, sommige saveareas in de stack kunnen wel degelijk veranderen.). ...
    www.cs.kun.nl/~hans/A1/studietaak%2010.htm cached

15. **http://members.tripod.com/~billlalonde/asm/asmps.txt**
    ... AS BASE USING ASMPS,R12 PROGRAM ADDRESSABILITY LA R0,WORKSIZE SET WORKAREA SIZE GETMAIN R,LV=(0) AND GET IT ST R13,4 (R1) * ST R1,8(R13) * CHAIN SAVEAREAS LR R13 ...
    members.tripod.com/~billlalonde/asm/asmps.txt cached

16. **IBM IT Education Services - Spain - MV03E Workshop del Lenguaje ...**
    ... Contenido, Concepto del lenguaje Ensamblador. Estructura de un programa escrito en lenguaje ensamblador. Enlace de "saveareas". "Cuándo es necesario?. ...
    www.ibm.com/services/learning/es/ta-iris.nsf/(ExtCourseNr)/MV03E cached

17. **MainFrameForum - 'U4083 - dsa corrupted with LE-enabled ASM - ...**
    ... The easist and fastest way to find out the 'wrong' pgm is, to use a debugger, which is able to watch all contence-change of that saveareas. ...
    mainframeforum.com/archive/1413/2002/3/152348 cached

18. **Seminare** (PDF)
    ... Processing - Kontrollblöcke - Mapping Makros - Job Control Blocks * Lademodule Linkage Editor - Parameter - Konvention - Saveareas - Reentrante Programmierung ...

www.cross-systems.de/pdf/Betriebssystem.pdf

19. **High Level Assembler: Toolkit Feature Technical Overview** (PDF)
    ... caller's registers lr r12,r15 Establish Base Using trace,r12 and tell the assembler st r13,savearea+4 Chain la r2,savearea the st r2,8(,r13) savearea la r15 ...
    www.share.org/proceedings/sh98/data/S8166A.PDF view as html

20. **TND-0202-10: Network User's Guide**
    ... 12(13) SAVE INCOMING REGISTERS BALR 10,0 ESTABLISH A BASE USING *,10 TELL THE ASSEMBLER ST 13,SAVEAREA+4 CHAIN THE LA 1,SAVEAREA TWO ST 1,8(13) SAVEAREAS LR 13 ...
    www.north-ridge.com/tnd420/TND02025.htm cached

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1. Review: Harry Potter and the Chamber of Secrets - Adventure ...
   ... of success. Electronic Arts remedied this in a few different ways. The first is the
   addition of multiple save areas. In the first ...
   www.adventurecollective.com/reviews/harrypotterandthechamberofsecrets.htm
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2. TCPaccess Planning Guide (PDF)
   Page 1. World Wide Web URL: http://www.interlink.com Interlink Computer
   Sciences, Inc. Tel: 510 657-9800 47370 Fremont Boulevard ...
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Within the architecture of MVT, MVS, OS/390 and z/OS, certain conventions have been established regarding the use of registers. These conventions will have been followed when you, the problem programmer, receive control from the operating system. They also should be followed for any routines, which you call, or for communications with the system (e.g. system macro calls, SVC's, returning control, etc.). Following these conventions will make your code easier for someone else to follow. Certain debugging aids are also available for those who adhere to standard conventions. In general, unless there is a STRONG reason to deviate, these conventions should be employed. The Operating System conventions are:

Return Register

Register 14 is called the "return register" and contains the address to which this routine is to return upon exit.

Entry Point Register

Register 15 is called the "entry point register" and contains the address through which this routine was entered. Note that temporary addressability my be established by using the following at the start of your routine:

```
    USING         ENTRYPOINT,R15
```

If this routine calls no other routines, register 15 may be used as a permanent base register. If this routine calls any other routine (including operating systems service) however, register 15 will be changed, and should not be used as a permanent base register. In this case, the sequence

```
    LR            BASEREG,R15
    USING         ENTRYPOINT,BASEREG
```

(Where BASEREG is any of registers 2-13) may be used to establish permanent addressability.

On return, register 15 may be used to return a code to indicate normal or error return. One frequently used technique is to set register 15 to zero on a normal return and set it to non-zero if some error condition occurred prior to return.

Register 13 is called the "save area register". It contains the address of an 18 full word area (on a full word boundary) within the calling routine. The routine called will use this area to save the contents of registers, to be able to return the registers intact to the calling program. This save area has a set format as follows:

Save Area Word	Description
Word 1	Used by PL/I and FORTRAN
Word 2	Address of the save area used by the calling program
Word 3	Address of the save area set up by the called program
Word 4	Address to which to return (register 14)
Word 5	Address of the entry point (register 15)
Word 6	Contents of register 0
Word 7	Contents of register 1
Word 8	Contents of register 2
Word 9	Contents of register 3
Word 10	Contents of register 4
Word 11	Contents of register 5
Word 12	Contents of register 6
Word 13	Contents of register 7
Word 14	Contents of register 8
Word 15	Contents of register 9
Word 16	Contents of register 10
Word 17	Contents of register 11
Word 18	Contents of register 12

Save areas are chained in a double-linked list. At any low-level routine, by tracing back through a chain of save areas links, one can eventually return to the system at the original point of call.

When your routine is entered, first you should save registers and then establish and like your own save area.

```
    STM           R14,R12,12(R13)        SAVE REGS 14,15 AND 0-12
    LA            R5,MYSAVE              GET ADDR OF MY SAVE AREA
    ST            R5,8(R13)              LINK CALLING PGM S.A. TO MINE
    ST            R13,4(R5)              LINK MY S.A. TO CALLING PGM'S
    LR            R13,R5                 POINT TO MY SAVE AREA NOW
```

On return

```
    XR            R15,R15                SET ZERO RETURN CODE
    L             R13,4(R13)             RETRIEVE ADDR OF CALLING PGM'S
                                         SAVE AREA
    LM            R14,R12,12(R13)        RESTORE REGISTERS
    BR            R14                    RETURN TO CALLER
```

Return Value Register

Register 0 is used to return a single result from some process (as in a FORTRAN function subprogram). Note: although you will probably not use this convention much, it is heavily used by the operating system. Register 0 cannot be guaranteed to be intact after executing some call to the system, as a system MACRO, or an SVC.

Argument List

Register 1 is the pointer to an argument list. It contains the address of the first of one or more full word entries (on consecutive full word boundaries). These entries are the addresses of arguments to be used by the calling routine.

If there are an indefinite number of arguments, (as with a routine which would accept one, two, or any number of arguments), the first bit of the last address is set to a 1. (this bit will not interfere with 24-bit addressing. This cannot be used when running in 31-bit mode.

The following example illustrates how to use the address list passed through register 1.

```
            LA        R1,ARGLIST          GET ARGUMENT LIST ADDR
            L         R15,=V(CALLRTN)     GET ADDRESS TO CALL
            BALR      R14,R15             CALL THE ROUTINE

ARGLIST     DC        A(ARG1)
            DC        A(ARG2)
            DC        X'80',AL3(ARG3)

CALLRTN     CSECT
            L         R2,0(R1)            GET ADDR OF NEXT ARG
            LTR       R1,R1               LAST ARG. IN LIST?
            BM        RETURN              IF YES, RETURN
            LA        R1,4(0,R1)          ELSE GET ADDR OF NEXT ARG
```

When a programmer receives control from the system, information from the PARM field of his/her EXEC card is passed via register 1. Register 1 points to a full word of storage. Bit 0 of this full word is set to 1 (to indicate the last-only-argument of the list). This full word contains the address of a half word. The half word is a count of the number of characters in the PARM field message, and these characters follow immediately after the half word count field. The contents of the half word may be picked up to use as a length count in an execute instruction, and the address of the half word may be used as a base to move the information characters of the PARM field.

Save Area Register

```
MYSAVE      DC        18F'0'              MY SAVE AREA
```

A calling program is known as a "higher routine", and the routine called is the "lower routine". Register 13 is always to point to an area whose contents may be destroyed.

An exception to the requirement that a routine must always establish a save area is that the lowest-level routine (the one which call no others) need not set up a save area. The reason for this is the save area is for the use of any called routines, but that the lowest-level routine will have no called routines.

Save Area Naming

The name convention is a means of having the EBCDIC form of the name of a routine appear at certain key places on dumps. To use this convention, the first four bytes of a routine must be a branch, on register 15 as a base register, which passed over a series of bytes. These bytes contain the EBCDIC form of the name of a routine, and also a length code for this name area. This example shows how to code a name field.

```
NAME        CSECT
            B         M+1+4(,R15)
            DC        X'M'
            DC        CLM'NAME'
            NEXT INSTRUCTION
```

The value of "M" must be odd, in order to make the next instruction properly aligned. An alternate approach used the convention on register 15:

```
NAME        CSECT
            USING     NAME,R15
            B         NEXTINST
            DC        X'M'
            DC        CLM'NAME'
NEXTINST    EQU       *
```

MVT, MVS and OS/390 follow these conventions strongly. In particular, the system often destroys the contents of registers 0, 1, 14, and 15 when it returns control from a system MACRO, an SVC, or another system function. One must save the contents of these registers before execution one of these functions (hard to locate errors will frequently occur after failure to do so).

It is a good idea to mark a save area upon exit. This is usually done by moving X'FF' into the first byte of the fourth word of the save area (the place register 14 was stored). Although this technique does not seriously affect the contents of the save area for reading a dump, this technique quickly shows what save areas are active and which are not active when reading a dump. This technique does not work in OS/390 when the address mode is 31-bits.

Register 13 must be kept as the save area pointer; however, by careful programming, it can also double as a base register. You may set up your own save area for this purpose by setting it high in the program, and following it by a using on register 13, referencing the name of the save area. For reserving the 18 full words on storage for a save area, us DC instead of DS. A constant of F'0', or F'-1' will quickly show in a dump if the save area was ever used.

SAVE and RETURN are two system MACROS which will eliminate must of the coding for saving and returning conventions. SAVE generates the code necessary to save a specified series of registers. The registers are specified as they would be for a STM instruction. In addition, the operand "T" will cause registers 14 and 15 to be stored, regardless of what other registers may also be saved from the pair specified. The following example will cause registers 5,6, ... 10, 14, and 15 to be saved.

            Save   (R5,R10),T

The RETURN MACRO will generate code to restore registers, insert a return code in register 15, flag the save area (X'FF' in full word 4), and branch back via register 14. The registers to be restored are coded as with save. If register 15 already has a return code in it and should not be restored, it is coded as RC=(R15); else RC=N may be coded, where N is some value to insert into register 15. The operand "T" causes the flag X'FF' to be inserted in the save area. The following code will restore registers 5,6, ... 10 to be reloaded, the save area to be flagged, and register 15 to be loaded with a value of 16.

            RETURN   (R5,R10),T,RC=16

NOTE both of these MACROS expect that register 13 will already be loaded with the address of the appropriate save area.

Credits: This document was a green-bar listing found from about 1972. It appears to be from the University of Pennsylvania.

— -+- — -+- — -+- — -+- — -+- — -+- — -+- — -+- — -+- — -+- — -+- — -+- —

YAHOO!search

Your Search: linkage savearea   [Search]   Advanced Web Search / Preferences

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TOP 20 WEB RESULTS out of about 183

1. **Introduction to External Subroutines** (MICROSOFT POWERPOINT)
... After we finish saving the necessary info with entry linkage commands, we set R13 to point at the current routine's savearea (step 7). ...
www.math.uni.lodz.pl/~miodekk/OS/390/ppt/13extsub.ppt view as html | more results from this site

2. **Mainframe Week - Code mvs**
... OS 0H WTO 'PCAUTHPG-E003 Unable to get a system linkage LX' B ... Set up exit requirements * * Load save area address and free savearea * * Restore caller's ARs ...
www.mainframeweek.com/code/showcode.php/0010/mw10mvs1.txt cached | more results from this site

3. **Xephon Archives**
... 05 A-SAVEAREA-COUNT PIC S9(4) COMP SYNC. ... 05 S-FCB-FOUND PIC X. /*** LINKAGE SECTION * * To allow ...
www.xephon.com/arcinframe.php/a012a02 cached | more results from this site

4. **PC Routines for Dummies** (PDF)
... 3 5 PC Routines for Dummies Linkage Stack A system-managed savearea Populated by Stacking PCs and by BAKR instruction PR to restore environment from stack and ...
www.cs.niu.edu/csci/567/ho/pcrtn.pdf view as html | more results from this site

5. **Overview**
... LINE1 00181000 PUT MYDCB,LINE2 00181100 PUT MYDCB,ENDIT 00181200 CLOSE MYDCB 00181300 * NOW PERFORM NORMAL RETURN LINKAGE. 00181400 L 13,SAVEAREA+4 00181500 ...
192.42.153.153:5000/~erodd/l421mvs2.htm cached

6. **OS/390 C/C++: XPLink, The Details** (PDF)
... Calling Convention Registers 6 and 7 used for linkage formerly Registers ... Point Address (optional) Environment (optional) Backchain (optional) Savearea (48 bytes ...
www.share.org/proceedings/sh96/data/S8121.PDF view as html | more results from this site

7. **http://www.flash.net/~mzelden/mvsfiles/jobcheck.txt**
... SAVE AREA R14 EQU 14 LINKAGE REGISTER (RETURN ADDRESS) R15 EQU 15 LINKAGE REGISTER (ENTRY ... SET UP BASE REGISTER LR R02,R13 SAVE ADDR OF PREVIOUS SAVEAREA LA R13 ...
www.flash.net/~mzelden/mvsfiles/jobcheck.txt cached

8. **Assembler Call, Load and Link**

... Standard member-to-member linkage is used. ... WTO ~ ASMASMA1 calling ASMASMAA without parameters...' LA R13,SAVEAREA SR R1,R1 CALL ASMASMAA WTO ~ ASMASMA1 ...
www.simotime.com/asmasm01.htm cached

9. **SAS/C SAMPLE: LOADMAA - C main dynamically loading an assembler ...**
... Standard OS Entry Linkage * *— STM 14 ... 13 Save callee save area addr LA 13,SAVEAREA Put mine ...
ftp.sas.com/techsup/download/SASC/sample/esm/LOADMAA.html cached | more results from this site

10. **Zee Guide to z/Architecture Zee Guide to z/Architecture** (PDF)
... ASCE (DAT) CR 7: Secondary ASCE (DAT) CR10: PER Range Start CR11: PER Range End CR12: Trace Entry Address CR13: Home ASCE (DAT) CR15: Linkage Stack Control ...
docs.share.org/proceedings/sh100/s2818.pdf view as html

11. **http://www.geocities.com/SiliconValley/Vista/9167/Sources/Loadm001.txt**
... W_CLEN(2),D$CESDLN CALL @OS#6HC,(W_CLEN,O$CLEN,2) BR R11 * — Linkage Editor Data ... LV=GETM_L2,A=GETM_A2 FREEMAIN RC,LV=GETM_LA=GETM_A L R13,SAVEAREA+4 RETURN ...
www.geocities.com/SiliconValley/Vista/9167/Sources/Loadm001.txt cached | more results from this site

12. **Seminare** (PDF)
... Hilfe der Dump Analyse - Debugger * Linkage Konventionen Parameterübergabe - Linkage Editor Optionen - Registerkonventionen - Savearea Verkettung - Reentrant ...
www.cross-systems.de/pdf/ibm-sprachen.pdf

13. **Assembler Coding in the z/OS Environment: Part I** (PDF)
... F1SA' * Your routine LA R0,3 Say what we want to get back from the linkage stack ESTA R0 ... After the BSM, you will replace R14 (31-bit version) in the savearea. ...
www.naspa.com/PDF/2002/0202%20PDF/T0202004.pdf more results from this site

14. **Horrible Things You Can Do With IDCAMS!**
... Lastly, SAVEAREA+4 contains C savearea address so that callback can directly return to caller. ... pragma linkage(myidcams,OS). #include <stdio.h>. ...
www.multi-platforms.com/Tips/HorribleIDCAMS1.htm cached

15. **CICS Program Debugging and Dump Analysis**
... Understand IBM's Standard Linkage Convention and the use of registers, the base and displacement addressing mechanism, and register savearea chaining. ...
www.netg.com/catalog/detail.asp?c=US13748 cached

16. **http://www.itc.virginia.edu/~drs8h/articles/lsrupdt.txt**
... 12 R13 EQU 13 R14 EQU 14 R15 EQU 15 OPENLSR CSECT STM R14,R12,12 (R13) ADDRESSABILITY USING OPENLSR,R12 AND STANDARD LINKAGE LR R12,R15 LA R11,SAVEAREA ST R13,4 ...
www.itc.virginia.edu/~drs8h/articles/lsrupdt.txt cached

17. **Tips & Newsletters-Search390.com, a TechTarget Web site**
... Lastly, SAVEAREA+4 contains C savearea address so that callback can directly return to caller. ... pragma linkage(myidcams,OS) #include struct {void *addr; int

---

len ...
search390.techtarget.com/tip/1,289483,sid10_gci535831,00.html cached

18. **Understanding and Debugging Kernel Panics**
... The initial stack pointer is the value of GPR1 in the savearea . The value of LR from the linkage area of the stack frame is printed, then the next stack frame ...
developer.apple.com/technotes/tn2002/tn2063.html cached | more results from this site

19. **CICS Debugging and Dump Analysis Mainframe Applications Course -**
...
... SAVEAREA's; Return Codes. Module ... Module 10 LOCATING LINKAGE SECTION VARIABLES AND INDEXES: Linkage Sections; Task Global Tables; Indexes. Module ...
www.datatrain.nl/training_mvs_os390_cics_debugging_and_dump_analysis_.htm cached

20. **Communication with Assembler Programs : Calling aC Program from ...**
... Calling aC main Function from Assembler via $MAINO MAINASM CSECT STM 14,12,12(13) standard OS entry linkage BALR 9,0 USING *,9 LR 14,13 LA 13,SAVEAREA ST 14,4 ...
support.sas.com/documentation/onlinedoc/sasc/doc/clug/zasscp.htm cached

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1. Introduction To Programming In An IBM Environment (PDF)
   ... Tasks And Transactions Resource Definition File System Definition File Program ... Understand IBM's Standard ... Addressing mechanism, and Register Savearea chaining ...
   www.dkl.com/pdf/datatrainpdfs/intro.pdf view as html

2. CPCS-I PTF06
   ... THIS APAR CORRECTS A S0C4 ABEND IN DKNDIXZD DUE TO SAVEAREA AND AMODE PROBLEMS ... IBM, OW43357, ... THESE BITS WERE SET BASED ON THE MDS FIELD DEFINITION RATHER THAN THE ...
   www.sei-mail.com/guru2067.htm cached

3. TCP/IP for VSE/ESA IBM Program Setup and Supplementary ... (PDF)
   ..... 216 iv TCP/IP for VSE/ESA IBM Program Setup and Supplementary Information Page 7. GSKSSOCCLOSE ... ... 359 Transient Data Definition ...
   ...
   www.ibm.com/servers/eserver/zseries/os/vse/pdf2/vse261/lestce21.pdf view as html
   | more results from this site

4. Xephon Archives
   ... by scanning the VTAM Resource Definition Table (RDT). ... nameless individuals on IBM's Dial-IBM Question service ... R15 ->ENTRY POINT LA R13,SAVEAREA - >SAVEAREA MVC 4 ...
   www.xephon.com/arcinframe.php/n012a04 cached | more results from this site

5. Reading PL/X in VPL Reading PL/X in VPL Part 1 Part 1 Session ... (PDF)
   ... See http://w3-3.ibm.com/legal ... defining block Unless nested block contains a definition of the ... instructions have equivalent BIls LM(R0,R4,Savearea); CDS (Old_Value ...
   www.share.org/proceedings/sh99/SHARE/data/S2863a.pdf view as html | more results from this site

6. PC Routines for Dummies (PDF)
   ... terminology not IBM's I Most IBM service routines ... Linkage Stack A system-managed savearea Populated by ... Table Entry: Individual PC Routine definition I Entry ...
   www.cs.niu.edu/cscl/567/ho/pcrtn.pdf view as html | more results from this site

7. Ulrich Weigand - [3.0.1 PATCH] S/390 important bugfixes
   ... 2001-08-10 Ulrich Weigand <uweigand@de.ibm.com> * config/s390/s390 ... TYPE_MODE (type) == SCmode) /* Mode of stack savearea. ... 771,780 — / The definition of this ...
   gcc.gnu.org/ml/gcc-patches/2001-08/msg00616.html cached | more results from this site

8. Microsoft PowerPoint - SE 313A - Unit 5 SSM 2K2-X-06.ppt (PDF)

... programs running for this process 5.1.1.1 IBM's Definition 7 ... An Informal Definition It is a number slot in the ... and 18 STOREregs <0,1,4,18>, SAVEAREA this will ...
www.engga.uwo.ca/electrical/bulletin/se_313a/SE313AUnit5.pdf view as html

9. Merry Christmas Programs
   ... Yes DDL, the Dungeon Definition Language from UCLA ... IBM 370 assembly */ How about this one (IBM 370 assembler ... r13) lr r12,r15 using xmas,r12 st r13,savearea+4 la ...
   www.4raccoons.com/~wayne/merry.html cached

10. Messages and Codes (PDF)
    ... . 625 Copyright IBM Corp. 1974, 2000 iii Page 6. ELXC Messages .... 660 Chapter 9. System Definition Messages ......
    www-3.ibm.com/software/data/tms/v5pdf/DFSD20C8.PDF view as html | more results from this site

11. Understanding and Debugging Kernel Panics
    ... PC varies depending on the definition of each ... is encountered or if a savearea exists for ... Environments For 32-Bit Microprocessors, IBM Microelectronics document ...
    developer.apple.com/technotes/tn2002/tn2063.html cached | more results from this site

12. Bixoft - Macro library - Bixoft eXtended Assembly language
    ... It does not mean that the IBM-supplied mapping macros are being replaced, rather ... SAVEAREA, MAPSAVE, Standard SAVE AREA. ... UCM, MAPUCM, Unit Controle Module definition. ...
    www.bixoft.nl/english/maclib.htm cached | more results from this site

13. Error Codes
    ... to be loaded into each of the workarea and savearea. ... nnn>) on line <mmm> of record definition file <dcffilename ... IBM believe there is a good reason for this but ...
    www.softekpartners.com/Error_Codes.html cached | more results from this site

14. Programmer's Guide (PDF)
    Page 1. High Level Assembler for MVS & VM & VSE IBM Programmer's Guide Release 3 SC26-4941-02 ... xii IBM High Level Assembler for MVS & VM & VSE Publications . ...
    www.math.uni.lodz.pl/~mlodekk/OS/390/asmp1002.pdf view as html

15. http://os390-mvs.hypermart.net/xephon/m092a08.txt
    ... do devt = devt 'Model_']Iden /* example : IBM 3390-9 */ end ... IRXARGTB IRXEVALB IGDSGD , STORAGE GROUP DEFINITION IEFSSSA , SSOB ... R12 LA R2,SAVE MY SAVEAREA ST R13 ...
    os390-mvs.hypermart.net/xephon/m092a08.txt cached

16. DUMPSCAN INSPECT Help Information
    (c) Copyright IBM Corporation 1990 ... This requires: o Block Definition File (BDF) containing CP control ... Registers SAVBK Chain SAVBK at address SAVEAREA at address ...
    www.ucc.uconn.edu/cgi-bin/cmshelp?DUMPSCAN%20INSPECT cached

17. Assembly Language Manual Chapter 7
    ... Notice that the fields of a structure definition may have ... 3. The IBM System/370 does not have a ... stored in a special memory area typically called the SAVEAREA.
    ...

---

members.cruzio.com/~bluejay/chap7.html cached

18. CICSPlex SM Messages and Codes (PDF)
    Page 1. CICS Transaction Server for OS/390 IBM CICSPlex SM Messages and Codes Release 3 GC33-0790-04 Page 2. Page 3. ... ... 185 Copyright IBM Corp. 1994, 1999 iii ...
    examples.oreilly.com/cics/CDROM/pdfs/cicsts/eyua100.pdf view as html

19. SPI Release 4.3 User's Guide and Reference
    ... more information on multiple job exits, refer to the IBM VSE/ESA ... AR SPI308I SPI Savearea is at X'nnnnnnnn ... A Date Definition Phase is used to define holidays and ...
    www.spursuits.com/mainframe/spi/spi43usersguide.htm cached

20. 2003 Newsgroup postings (3/31 - 4/8) Lynn Wheeler
    ... for allocation that allowed finer control definition on a ... Architecture of the IBM System/360 IBM Journal of ... could effectively get by with a static savearea. ...
    www.garlic.com/~lynn/2003f.html cached

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(i) Did you mean: save area layout?

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1. base/sys/arch/i486/LAYOUT
   ... bracketed by begin/end statements. The * LAYOUT file needs to define SaveArea
   and FloatSaveArea. * Otherwise, entries take the form ...
   www.eros-os.org/eros-src/base/sys/arch/i486/LAYOUT cached | more results from
   this site

2. OS/390 C/C++: XPLink, The Details (PDF)
   ... Stack Layout Detail For future use Current Register 4 possibly in ... Entry Point
   Address (optional) Environment (optional) Backchain (optional) Savearea (48
   bytes ...
   www.share.org/proceedings/sh96/data/S8121.PDF view as html | more results from
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3. CICS Program Debugging and Dump Analysis
   ... Understand the layout of a ... Standard Linkage Convention and the use of
   registers, the base and displacement addressing mechanism, and register savearea
   chaining ...
   www.netg.com/catalog/detail.asp?c=US13748 cached

4. Mainframe Week - Code mvs
   ... 8(R13) LA R13,SAVEAREA *— MAIN LINE ... ESTABLISH CALLING PANEL
   LAYOUT * ** DETERMINE NUMBER ...
   www.mainframeweek.com/code/showcode.php/0048/mw48mvs3.txt cached | more
   results from this site

5. CICS Debugging and Dump Analysis Mainframe Applications Course -
   ...
   ... Operating System "Snap" Dump; Dump Layout. Module 4 LOCATING CICS
   AREAS OF INTEREST: CICS Areas Useful for Debugging; CWA Length; ...
   SAVEAREA's; Return Codes. ...
   www.datatrain.nl/training_mvs_os390_cics_debugging_and_dump_analysis_.htm
   cached

6. Introduction To Programming In An IBM Environment (PDF)
   ... Course, the student will be able to - - Understand the layout of a ... Registers, the
   Base and Displacement Addressing mechanism, and Register Savearea chaining. ...

   www.dkl.com/pdf/datatrainpdfs/intro.pdf view as html

7. Migrating VSE System Applications to the Web: Part II - Porting ...
   (PDF)
   ... R1 = Buffer address for returning the data * R2 = ACBLOK pointer * * The
   ACBLOK has a layout as follows ... * Incoming:R2=ACBLOK address *
   R13=Savearea * R15=Base ...
   www.naspa.com/PDF/2000/0200%20TS%20PDFs/T0002003.pdf more results from

---

this site

8. http://www.iuassn.com/UCL/viewdb01.txt
   ... DS 0CL7 PARMLRYY DS CL4 PARMLRDD DS CL3 PARMHALC DS CL3
   PARMNOEX DS CL2 PARMNOSP DS CL16 DS CL98 VS24BIT DSECT
   SAVEAREA DS 18F DWORD ... 008200 01 Header-Layout. ...
   www.iuassn.com/UCL/viewdb01.txt cached

9. Herne's SMAUG Building Pages - Building Rooms
   ... Remember that most important command: SAVEAREA ... doing any building, a
   Creator level Immortal should make a basic outline of their intended area layout on
   paper ...
   webhome.idirect.com/~helspawn/smaug/olc-rooms.html cached

10. Condition Handling with Language Environment (PDF)
    ... This 12- byte code is the same layout as the feedback code returned from ...
    Assembler it is no longer appropriate to have the traditional 18F savearea, and you
    no ...
    www.ibm.com/servers/eserver/zseries/zos/le/conference/pdf/smn8234.pdf view as
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11. Finalist (PDF)
    ... automatically initialized. Check the retcode field prior to the errinfo field. Errinfo
    has the layout shown next. Physical Length ...
    ummvsb.missouri.edu/pb/final/CHAPTR10.PDF view as html | more results from this
    site

12. Excalibur.. It's time to prove yourself
    ... level Immortal should make a basic outline of their intended area layout on
    paper ... Most importantly, be sure to SAVEAREA after you make any additions or
    changes ...
    excalibur.sourceforge.net/docs/getdoc.php?docid=4 cached

13. Finalist (PDF)
    ... Errinfo has the layout shown next. ... 30 27-56 Reserved for future expansion.
    Retinfo has the following layout for this function: Physical Length From/To Position
    www.state.ak.us/local/akpages/ADMIN/info/lnlpdf/mf/batch/chaptr10.pdf view as html

14. http://www.kehei.com/public/archive/wiki0125.pl
    ... ENV('SAVEAREA') : ".textdir&quot ... FILE, "> $name"); print
    FILE $text; close(FILE); } # templates can have per-db layout sub readTemplate ...
    www.kehel.com/public/archive/wiki0125.pl cached

15. Assembler to C Migration using the FermaT Transformation System
    (PDF)
    ... implemented in C, or to meet customers' requirements on the C layout or
    features ... On the other hand, if savearea is declared as an array of fullwords and
    the ...
    www.dur.ac.uk/martin.ward/martin/papers/icsm99-t.pdf view as html

16. Xephon Archives
    ... DC X'08',CL8'VPO13010' ACBTEND DC X'00',8X'FF' LTORG * * ** COMMAREA
    LAYOUT COMMAREA DSECT CMDO ... SAVE (14,12),,* LR R12,R15 USING
    VPPPO,R12 ST R13,SAVEAREA+4 LR ...
    www.xephon.com/arcinframe.php/n019a02 cached | more results from this site

17. http://web.ukonline.co.uk/dougie.lawson/gse_files/dfsaoe00.txt

---

... AMODE 31 DFSAOE00 RMODE ANY * * INITIALISATION CODE * USING
SAVEAREA,R13 SAVE (14 ... RESERVED SPACE DYNLNG EQU *-DYNWRK * *
INPUT SEGMENT LAYOUT * SEGMIN DSECT DS ...
web.ukonline.co.uk/dougie.lawson/gse_files/dfsaoe00.txt cached

18. Assembler to C Migration using the FermaT Transformation System
    (PDF)
    ... implemented in C, or to meet customers' requirements on the C layout or
    features ... On the other hand, if savearea is declared as an array of fullwords and
    the ...
    www.smitd.com/icsm99-t.pdf view as html

19. http://www.planetmvs.com/unxmit/file005.txt
    ... OF RECORD 00000660 SPACE 3 00000670 COPYR3 DSECT , RECORD 3-N
    LAYOUT 00000680 R3F ... USING IEBCOPY,R12,R11 00001280 SPACE ,
    00001290 LA R15,SAVEAREA 00001300 ST ...
    www.planetmvs.com/unxmit/file005.txt cached

20. Re: [SMAUG] Building problem (Seg Fault)
    ... > After spending a couple of hours throwing together the basic layout of my ...
    alterations but only method replacements in act_info.c) I did > savearea and then ...
    www.shadow-lands.com/sml/2001/msg03461.html cached

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www.electropedicbeds.com

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1. **savearea** 몔
savearea. ... I have done all everything you guys have suggested but everytime I try to **savearea** it says: Build: Raistlin: **savearea** Log: Saving Raistlin.are... ...
www.shadow-lands.com/sml/1997/msg05159.html cached | more results from this site

2. [EROS-Arch] **SaveArea**.h 몔
[EROS-Arch] **SaveArea**.h. Jonathan S. Shapiro ... **SaveArea**.h is a thin wrapper around fixregs.h, which is machine generated. Strictly speaking, you ...
www.eros-os.org/pipermail/eros-arch/2001-March/002854.html cached | more results from this site

3. ClipX - Force 4.0 Reference - **savearea()** save screen area 몔
... **savearea()** Save screen area ----- Declaration screen.hdr Syntax proc **savearea** extern ... ·
www.clipx.net/ng/force40/ng12512f.php cached | more results from this site

4. http://www.geocities.com/SiliconValley/Vista/9167/Sources/os_6hc.txt 몔
R03 ST R13,**SAVEAREA**+4 Save A(OLD **SaveArea**) LR R12,R13 LA R13,**SAVEAREA** ST R13,8(R12) Save A(NEW **SaveArea**) * --- from characher --- L R07,0(R01) * --- to ...
www.geocities.com/SiliconValley/Vista/9167/Sources/os_6hc.txt cached | more results from this site

5. http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt (MICROSOFT POWERPOINT) 몔
... Global Data (p. 123): for process management, IPC and disk I/O. Note **SaveArea**, ProcessDescriptor, & interrupt vector pointers. ... struct **SaveArea** {. ...
www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt view as html | more results from this site

6. **savearea** 몔
浜松市消防団第２０分団の管轄は和田地区（和田町、天竜 川町、篠ケ瀬町、北島 町、薬師町、薬新町、安間町、安新 町 ...
homepage3.nifty.com/hm-fire20/**savearea**.htm cached

7. Python: module rox.saving 몔
... Classes. gtk.Dialog(gtk.Window) SaveBox StringSaver(SaveBox, Saveable)

exceptions.Exception AbortSave Saveable SaveFilter gtk.VBox(gtk.Box)
SaveArea ...
rox.sourceforge.net/rox_lib_docs/rox.saving.html cached

8. BAL & BALR (MICROSOFT POWERPOINT) 🕮
   ... Example: STM R6,R10,**SAVEAREA** Save registers. . . . ... Wraps around if order
   seems backwards: STM R14,R1,**SAVEAREA** .... **SAVEAREA** DS 4F Holds R14,
   R15, R0, R1. 9. ...
   www.math.uni.lodz.pl/~miodekk/OS/390/ppt/8subrtns.ppt view as html | more results
   from this site

9. Gammon Software Solutions forum 🕮
   ... **savearea**. ... kay when I try to use **savearea** to save a newly assign area it says
   your area is not loaded I have a building directory so that's not the problem. ...
   www.gammon.com.au/forum/bbshowpost.php?bbsubject_id=390 cached | more
   results from this site

10. Lecture 1: Course Introduction and Overview (MICROSOFT
    POWERPOINT) 🕮
    ... /. }. sioint_stub: sw [**savearea**], R0. sw [**savearea**+4], R1. sw [**savearea**+8], R2. ;
    [. . .. sw [**savearea**+32], oldPC. call sio_handler. lw oldPC, [**savearea**+32]. ; [. . ...
    www.cc.gatech.edu/classes/AY2003/cs2200_spring/Slides/Lec10-proc.ppt view as
    html | more results from this site

11. Xephon Archives 🕮
    ... XA SP2.1). FINDSYMB ASSEMBLE TITLE 'FINDSYMB - Find CP symbol' PRINT
    GEN FINDSYMB CSECT DS 0H STM R14,R12,12(R13) * Save caller's regs in his
    savearea BALR R12 ...
    www.xephon.com/arcinframe.php/v079a01 cached | more results from this site

12. http://www.cs.wpi.edu/~claypool/courses/502-
    Su03/samples/SOS/timer.c 🕮
    #include "system.h" /* This procedure is called when a timer interrupt occurs. */ void
    TimerInterruptHandler(void) { struct **SaveArea** *savearea; if ...
    www.cs.wpi.edu/~claypool/courses/502-Su03/samples/SOS/timer.c cached | more
    results from this site

13. Mainframe Week - Code racf 🕮
    ... RACFDGEN.&SYSDATE SAVE REGISTERS LR R12,R15 | LA R11,4095(R12) |
    INITIALIZATION OF BASE REGISTERS LA R11,1(R11) | LR R2,R13 ADDRESS OF
    PRECEDING **SAVEAREA** LA R13 ...
    www.mainframeweek.com/code/showcode.php/0013/mw13racf1.txt cached | more
    results from this site

14. http://www.netlib.org/go/ibmblas 🕮
    ... PRINT NOGEN &NAME CSECT EQUATE AIF ('&TRACE' NE 'YES').L1 HSA
    EQU 4 . HIGHER **SAVEAREA** LSA EQU 8 . LOWER **SAVEAREA** .L1 ANOP
    &CALLQ SETB ('&TRACE' EQ 'YES ...
    www.netlib.org/go/ibmblas cached

15. http://elib.zib.de/netlib/go/ibmblas 🕮
    ... PRINT NOGEN &amp;NAME CSECT EQUATE AIF ('&amp;TRACE' NE
    'YES').L1 HSA EQU 4 . HIGHER **SAVEAREA** LSA EQU 8 . LOWER
    SAVEAREA .L1 ANOP &amp;CALLQ SETB ...
    elib.zib.de/netlib/go/ibmblas cached

16. Chapter 15 - A USING Instruction Tutorial (PDF) 🕮

... MAIN CSECT 2 STMINST STM 14,12,12(13) Save regs; note explicit base (13) 3 USING MAIN,15 R15 will have A(MAIN) at execution 4 LAINST LA 14,**SAVEAREA** Get address ...
www.cs.niu.edu/~mstack/csci/using.pdf view as html | more results from this site

17. **Horrible Things You Can Do With IDCAMS!**
... Lastly, **SAVEAREA**+4 contains C **savearea** address so that callback can directly return to caller. ... ST R7,BASEADDR Save base. ST R13,**SAVEAREA**+4 Backward pointer. ...
www.multi-platforms.com/Tips/HorribleIDCAMS1.htm cached

18. **22C:116, Homework 5 Solutions, Fall 2001**
... cause not one of addr_fault or access_fault, this is not a page fault, do other things not related to this assignment else page = pagefield(**savearea**.va) if ...
www.cs.uiowa.edu/~jones/opsys/fall01/hw/05sol.html cached

19. **Tips & Newsletters-Search390.com, a TechTarget Web site**
... routines can access. Lastly, **SAVEAREA**+4 contains C **savearea** address so that callback can directly return to caller. The example ...
search390.techtarget.com/tip/1,289483,sid10_gci535831,00.html cached | more results from this site

20. **http://members.tripod.co.uk/nvcs/Samples/nvcsget.htm**
... EQU 12 R13 EQU 13 R14 EQU 14 R15 EQU 15 EJECT STM R14,R12,12(R13) * SAVE ENTRY REGS BALR R12,R0 * ESTABLISH BASE USING *,R12 LR R2,R13 * CALLER **SAVEAREA** LA R13 ...
members.tripod.co.uk/nvcs/Samples/nvcsget.htm cached

Did you mean to search for: **save area**?

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Search Technology provided by Google

YAHOO! search

Your Search: IBM savearea [Search] Advanced Web Search
Preferences

[Web] Directory News NEW Yellow Pages Images What's New | Send Feedback

TOP 20 WEB RESULTS out of about 260

1. Sketch of S/360 Subroutines – Mark Smotherman
   ... This mainframe architecture lives on today in the IBM zSeries architecture (having been ... pseudo-op to tell assembler to use r12 st r13,savearea+4 l store ...
   www.cs.clemson.edu/~mark/subroutines/s360.html cached | more results from this site

2. IBM Linux Technology Center : Open Source Patches
   ... giveup_fpu will get a pointer to our (current's) last register savearea via current->thread.regs and using that pointer will turn off the MSR_FP, MSR_FE0 and ...
   oss.software.ibm.com/linux/patches/?patch_id=169 cached

3. Re: Porting Arla to Linux/390
   ... savearea *context); And the savearea struct is actually called lwp_context these days... /Johan: References: Porting Arla to Linux/390: From: maydin@us.ibm.com. Re ...
   www.stacken.kth.se/lists/arla-drinkers/2000-07/msg00035.html cached | more results from this site

4. IBM COBOL for MVS & VM Version 1 Release 2 Performance Tuning ... (PDF)
   IBM COBOL for MVS & VM Version 1 Release 2 Performance Tuning January 26, 1996 RJ Arellanes IBM Corporation Software Solutions 555 Bailey Avenue San Jose, CA ...
   www.ibm.com/software/ad/cobol/library/booksabc/cobpl120.pdf view as html | more results from this site

5. Introduction To Programming In An IBM Environment (PDF)
   ... Understand IBM's Standard Linkage Convention and the use of Registers, the Base and Displacement Addressing mechanism, and Register Savearea chaining. ...
   www.dkd.com/pdf/datatrainpdfs/intro.pdf view as html

6. http://www.wiu.edu/users/mfll/cs310/unit76.html
   ... The template for a subroutine obeying the IBM-convention is: subname equ * STM 14 ... area allocated by the subroutine subroutine call template LA 13,savearea LA 15 ...
   www.wiu.edu/users/mfll/cs310/unit76.html cached

7. CPCSGURU - IBM supplied Usermods for CPCS PTF22
   ... IBM Provided "Usermods" for CPCS PTF22. ... level of PQ36627 This USERMOD corrects several abend codes which could occur due to a corrupted savearea address when ...
   www.sei-mail.com/guru2227.htm cached | more results from this site

8. The Future z/OS Application: Virtually an Endless Space (PDF)
   ... Page 8. zSe ries zSeries IBM New Savearea Formats F2SA - reserved format code F3SA - reserved format code F4SA - 144-byte savearea; caller's registers saved in ...

---

www.share.org/proceedings/SH98/data/S2851.PDF view as html | more results from this site

9. Assembler Coding in the z/OS Environment: Part I (PDF)
   ... After the BSM, you will replace R14 (31-bit version) in the savearea. The IBM RETURN macro is also replaced with code that matches the RETURN macro except in ...
   www.naspa.com/PDF/2002/0202%20PDF/T0202004.pdf more results from this site

10. Assembler Call, Load and Link
    ... This program will run an an IBM Mainframe using MVS or a PC ... WTO " ASMASMA1 calling ASMASMAA without parameters...' LA R13,SAVEAREA SR R1,R1 CALL ASMASMAA ...
    www.simotime.com/asmasm01.htm cached

11. http://openbsd.secsup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c
    ... ALL ** IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS, IN NO EVENT SHALL IBM ** BE LIABLE ... savecontext(int (*ep)(), struct lwp_context *savearea, char *sp ...
    openbsd.secsup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c cached

12. PC Routines for Dummies (PDF)
    ... Space Switching Global: I My terminology not IBM's I Most IBM service routines ... 5 PC Routines for Dummies Linkage Stack A system-managed savearea Populated by ...
    www.cs.niu.edu/csci/567/ho/pcrtn.pdf view as html | more results from this site

13. Dave's S/390* Assembler FAQ page
    ... savearea Bytes 136-143: address of next savearea This entry was taken from a post by Peter Relson (z/OS Core Technology Design) on the IBM-Main newsgroup ...
    www.planetmvs.com/hlasm/s390faq.html cached | more results from this site

14. http://www.freeusp.org/Arco/Data/sparc/r4toi2
    ... 00260000 *A 00270000 *A 00280000 *A CONVERT 32 BIT IBM FLOATING POINT VALUES TO ... 00780000 STM R14,R12,12(R13) SAVE REGISTERS 00790000 LA R12,SAVEAREA POINTER TO ...
    www.freeusp.org/Arco/Data/sparc/r4toi2 cached | more results from this site

15. Zee Guide to z/Architecture Zee Guide to z/Architecture (PDF)
    ... for zArchitecture Designated by ASCE bit 58 (R) Does not undergo address translation: The virtual addresses treated as real DAT tables not used IBM calls this ...
    docs.share.org/proceedings/sh100/s2818.pdf view as html

16. LTC bulletin: May 21, 2001
    ... giveup_fpu got a pointer to the (current's) last register savearea via current->thread ... Maneesh has been working with the IBM Software Lab, India since December ...
    www-106.ibm.com/developerworks/linux/library/l-ltc3.html cached

17. Xephon Archives
    ... My thanks to the nameless individuals on IBM's Dial-IBM Question service and to Bill Earls ... FOR PROGRAM LR R12,R15 ->ENTRY POINT LA R13,SAVEAREA ->SAVEAREA MVC 4 ...
    www.xephon.com/arcinframe.php/n012a04 cached | more results from this site

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18. Mainframe Week - Code db2
    ... PILLAY ** FOR MORE INFORMATION...: DB2 ADMIN GUIDE ** APAR INFO : WWW.SOFTWARE.IBM.COM/DATA ... ENTRY POINT USING I258,R12 USE R4 AS BASE XC SAVEAREA(72),SAVEAREA ...
    www.mainframeweek.com/code/showcode.php/0015/mw15db23.txt cached | more results from this site

19. Controle van het besturingsverloop (MICROSOFT POWERPOINT)
    ... adr(M). adr(I). .... K. L. M. I. Procedure-oproep op IBM 360: alternatief. LA 13,SAVEAREA. L 15,ADRSOM. LA 14,RETOUR. BALR 1,15. ARGLIST DC A(K). DC A(L). DC A(M). DC A(I). ...
    www.elis.rug.ac.be/~kdb/cai/h5.ppt view as html

20. WRP60
    ... In MVS, Register 13 is the savearea pointer. ... ===== Newsgroups: bit.listserv.ibm-main From: edjaffe@netcom ...
    home.xnet.com/~msk/futuresource/hamasaki/wrp60.html cached

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① Did you mean: "multiple save area"?

① We didn't find any Web pages containing "multiple savearea".

Tip:
Check your spelling.
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Broaden your search by using fewer words.

http://search.yahoo.com/bin/search?p=%22multiple+savearea%22&ei=UTF-8

7/29/03

YaHOO! search

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YAHOO! search

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(i) Did you mean: **select save area?**

TOP 20 WEB RESULTS out of about 185

1. **Mainframe Week - Code mvs**
   ... DEPENDING ON SPLIT SCREEN) IF THE REXX CONTAINING THE ">>>
   SELECT PGM IS ... STORAGE USING PGMAREA,R11 ESTABLISH DATA AREA
   ADDRESSABILITY ST R13,SAVEAREA+4 . SAVE ...
   www.mainframeweek.com/code/showcode.php/0015/mw15mvs2.txt cached | more
   results from this site

2. **Xephon Archives**
   ... SELECT ESDS-FILE ASSIGN TO AS-HMZNDIAG ORGANIZATION IS
   SEQUENTIAL. SELECT RBA-FILE ASSIGN TO HMZNPROC. ... 05 A-SAVEAREA-
   COUNT PIC S9(4) COMP SYNC. 01 CONSTANTS. ...
   www.xephon.com/archinframe.php/a012a02 cached | more results from this site

3. **IOF Q&A Message Board**
   ... &STR() THEN DO : &I BROWSE : ENDO : ELSE DO : &I SELECT : EXCL
   DDNAME NE &DDNAME : 1 SELECT : ENDO : FIND ... SPACE , : LA
   R1,WRKSAVE Chain new saveares to : ST ...
   www.triangle-systems.com/wwwboard/messages/484.shtml cached

4. **Chapter 15 - A USING Instruction Tutorial** (PDF)
   ... has only one USING available for each instruction from which to select a base ...
   this program, par ticularly how it handles: 4 LAINST LA 14,SAVEAREA Get
   address ...
   www.cs.niu.edu/~mstack/csci/using.pdf view as html

5. **COPYKEYS: An ISPF Solution** (PDF)
   ... Upon return from the SELECT, the table is again TBDISPL'd, this time with ... Stor
   Address-< R10 */ USING STORAGE,R10 /* Base Storage */ ST
   R13,SAVEAREA+4 /* Set ...
   www.naspa.com/PDF/99/T9910003.pdf

6. **http://www.sillysot.com/ftp/editu.txt**
   ... a').' Signal out End End End Address tso 'ALLOC F(A1) DA(indataset) REU SHR'
   'SELECT PGM(TC ... GETMAIN RU,LV=(0),SP=0,LOC=BELOW ST 1,8(13) SAVE
   SAVEAREA ADDR IN ...
   www.sillysot.com/ftp/editu.txt cached | more results from this site

7. **06-01_shoshiki**
   ... PasteSpecial xlPasteFormats .Interior.ColorIndex = 8 End With Target.Select Set
   PrevCell = ActiveCell End If Target.Select Set SaveArea =
   Nothing .CutCopyMode ...
   www.hm2.aitai.ne.jp/~naka95/Excel_Note/06-01_shoshiki.htm cached

8. **SuperPaint Plug-in**

... Go back to the drop window, select the entire window, and command Capture ...
ForgetMask = 8; (discard all mask and saved) { data } type SaveArea = record
(area ...
www.mactech.com/articles/mactech/Vol.08/08.01/SuperPaintPlug-in/ cached

9. **Z4Select** (PDF)
   ... OUTREC B MAINLOOP WRTSELC DS 0H PUT SELECT,OUTREC B
   MAINLOOP RETURN DS 0H MVI Z4FUNC,C'9' BAL R7,CALLZ4 CLOSE
   (FILEIN,,SELECT,,NOSELECT) * L R13,SAVEAREA+4 LM ...
   www.state.ak.us/local/akpages/ADMIN/info/tnlpdf/mf/z4select/chaptr05.pdf view as
   html | more results from this site

10. **Bixoft - BXAIO - Static glue module**
    ... Set parameter version to 07 03700000 MVC LNSFILES,=C'00000010' * Select all
    files ... BR R14 * Return to mainline 03750000 * 03760000 EJECT 03770000
    SAVEAREA DS 9D ...
    www.bixoft.nl/goodies/bxaio.htm cached | more results from this site

11. **http://os390-mvs.hypermart.net/xephon/m092a08.txt**
    ... ucba) retc = length(lsp) if retc >= 36 then retc = 0 bx = '7' perc = '???' select when
    retc ... SYSVTOC ENQ' BASR R12,0 USING *,R12 LA R2,SAVE MY SAVEAREA
    ST R13 ...
    os390-mvs.hypermart.net/xephon/m092a08.txt cached

12. **Sub ScoreBasedAllocation()** (MICROSOFT WORD)
    ... SolverSolve userFinish:=False. SolverSave saveArea:=Range("A100"). Range
    ("B2").Offset(0, Numport + 1).Copy. Range(Cells(2, 3), Cells(2, Numport +
    2)).Select. ...
    faculty.fuqua.duke.edu/~charvey/Teaching/BA453_1999/Ltom/Code.doc view as
    html

13. **Dave's S/390* Assembler FAQ page**
    ... The final three X'20's are the rest of the digit select characters necessary for ...
    such as what to do if your caller provides a 72-byte savearea, and you need to ...
    www.planetmvs.com/hlasm/s390faq.html cached

14. **http://www.geocities.com/SiliconValley/Vista/9167/Sources/rv_080.txt**
    ... CLI @SELECT,C'R' BE @RESTORE CLI @SELECT,C'A ... B @TERM8 *----
    TERM8 EQU * L R13,SAVEAREA+4 RETURN ...
    www.geocities.com/SiliconValley/Vista/9167/Sources/rv_080.txt cached

15. **Using the 3494 Tape Library under VSE/ESA**
    ... With a new EXTRACT service it is possible to select a free 3490E device in a ...
    R7,MYLIB LA R8,MYSERVL LA R9,WRITEFL LA R10,TCATEG LA
    R13,SAVEAREA USING IJJLBSER ...
    www.ibm.com/servers/eserver/zseries/os/vse/support/at/vse5sup.htm cached | more
    results from this site

16. **Finalist** (PDF)
    ... COMMENT Card 4-5 CTYCORR Card 4-6 FIELDS Card 4-6 INPUT Cards 4-10
    OUTPUT Cards 4-10 OPTION Cards 4-11 OUTBLD Cards 4-14 PRINTMOD Card 4-
    15 SELECT/EXCLUD Card 4 ...
    ummvsb.missouri.edu/pb/final/CHAPTR04.PDF view as html

17. **ClipX - Force 4.0 Reference - pickdisp() display a string list ...**
    ... pSub, "item &7") pickadd(pSub, "item &8") pickadd(pSub, "item &9") savearea
    (4, 31 ... otherwise nCh := inkey() do case // We use'Enter' here to select an item ...

---

www.clipx.net/ng/force40/ng9f106.php cached

18. **Reading PL/X in VPL Reading PL/X in VPL Part 1 Part 1 Session ...**
    (PDF)
    ... path, or a DO/SELECT Decremented at end of path, or on END of DO/SELECT ...
    set (over 160) of instructions have equivalent Blls LM(R0,R4,Savearea); CDS
    (Old_Value ...
    www.share.org/proceedings/sh99/SHARE/data/S2883a.pdf view as html

19. **Ausam/sys/conf/powerf.s-new**
    ... 2,sp / get a one word temp stack / mov $rest,mover / select desired mov ...
    volatile locations described by 'tosave' copies: .if .START - 1 mov $savearea,r1 /
    save ...
    minnie.tuhs.org/UnixTree/Ausam/sys/conf/powerf.s-new.html cached

20. **http://www.iuassn.com/UCL/viewdb01.txt**
    ... DS CL3 PARMNOEX DS CL2 PARMNOSP DS CL16 DS CL98 VS24BIT DSECT
    SAVEAREA DS 18F ... 001300 Select Sys001-File Assign To Sys001 001400 File
    Status is Sys001-Status. ...
    www.iuassn.com/UCL/viewdb01.txt cached

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